

PATENT NUMBER

U.S. **UTILITY** Patent Application

**Q.I.P.E.**

PATENT DATE

SCANNED

## Q.A

APPLICATION NO.  
09/927573

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CLASS  
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SUBCLASS

ART UNIT

**EXAMINER**

~~281~~  
2826

Backlog

## APPLICANTS

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Memory cell, memory cell configuration and fabrication method

**TITLE**

PTO-2040  
12/89

## ISSUING CLASSIFICATION

[illegible]☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner)		<b>ISSUE FEE</b>	
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